

## IN THE CLAIMS

1. (original) A switch for regulating the substrate potential of an integrated circuit comprising:

a first control input coupled to a first N-well bias supply line;  
a second control input coupled to a substrate bias supply line;  
a switched terminal coupled to a ground;  
a switched terminal coupled to said substrate bias supply line; and  
an output terminal coupled to a P-type substrate.

2. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said first N-well bias supply line.

3. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line.

4. (original) The switch of Claim 1, further comprising a second control input coupled to a second N-well bias supply line.

5. (original) The switch of Claim 4, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said second N-well bias supply line.

6. (original) The switch of Claim 4, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line.

7. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said substrate bias supply line when a substrate bias voltage is present on said substrate bias supply line and there is no bias voltage present on said N-well bias line.

8. (original) The switch of Claim 1, wherein said switch is operable to electrically couple said P-type substrate to said ground when a substrate bias voltage is present on said substrate bias supply line and there is no bias voltage present on said N-well bias line.

9. (original) A switch for regulating the substrate potential of an integrated circuit comprising:

- a first control input coupled to a first N-well bias supply line;
- a second control input coupled to a substrate bias supply line;
- a switched terminal coupled to a ground;
- a switched terminal coupled to a charge pump enable line; and
- an output terminal coupled to a P-type substrate.

10. (original) The switch of Claim 9, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said first N-well bias supply line.

11. (original) The switch of Claim 9, wherein said switch is operable to isolate said P-type substrate from ground when an enable signal is present on said charge pump enable line.

12. (original) The switch of Claim 9, further comprising a second control input coupled to a second N-well bias supply line.

13. (original) The switch of Claim 12, wherein said switch is operable to electrically couple said P-type substrate to said ground when a bias voltage is present on said second N-well bias supply line.

14. (original) The switch of Claim 12, wherein said switch is operable to electrically isolate said P-type substrate from ground when an enable signal is present on said charge pump enable line.

Claims 15-20 (canceled) (restriction)